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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10675776
Filing Date	2003-09-30
First Named Inventor	DeWitt, Jr. et al.
Art Unit	2193
Examiner Name	Vu, Tuan A.
Attorney Docket Number	AUS920030481US1

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AY	1	6542985		2003-04-01	Johnson et al.	
	2	4316245		1982-02-16	Luu et al.	
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	7	5659679		1997-08-19	Alpert et al.	
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


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	1	20040194076	A1	2004-09-01	Comp et al.	
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	1	RAMIREZ et al., "The Effect of Code Reordering on Branch Prediction", Proceedings of the International Conference on Parallel Architectures and Compilation Techniques, October 2000, pp. 189-198.	<input type="checkbox"/>
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3	CONTE et al., "Accurate and Practical Profile-Driven Compilation Using the Profile Buffer", Proceedings of the 29th Annual ACM/IEEE International Symposium on Microarchitecture, Paris, France, 1996, pp. 36-45.	<input type="checkbox"/>
4	CONTE et al., "Using Branch Handling Hardware to Support Profile-Driven Optimization", Proceedings of the 27th Annual International Symposium on Microarchitecture, San Jose CA, 1994, pp. 12-21.	<input type="checkbox"/>
5	FISHER, "Trace Scheduling: A Technique for Global Microcode Compaction", IEEE Transactions on Computers, Vol. C30, No. 7, July 1981, pp. 478-490.	<input type="checkbox"/>
6	CHANG et al., "Using Profile Information to Assist Classic Code Optimizations", Software Pract. Exper. 21, December 1991, pp. 1301-1321.	<input type="checkbox"/>
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8	AHO et al., "Compilers: Principles, Techniques, and Tools", Addison-Wesley, 1988, pp. 488-497.	<input type="checkbox"/>
9	INTEL, "Intel IA-64 Architecture Software Developer's Manual", Revision 1.1, Vol. 4, No. 245320.002, July 2001.	<input type="checkbox"/>

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	2	6832296	B2	2004-12-14	Hooker	
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WAT	9	6256771	B1	2001-07-03	O'Neil et al.	
	10	6966057	B2	2005-11-15	Lueh	
	11	7131115	B2	2006-10-31	Hundt et al.	
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	13	5544342		1996-08-06	Dean	
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	2	20040049712	A1	2004-03-11	Betker et al.	
WAT	3	20030225917	A1	2003-12-04	Partamian et al.	

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	4	20030135719	A1	2003-07-17	DeWitt, Jr. et al.	
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